

NOT RECOMMENDED FOR NEW DESIGN NO ALTERNATIVE PART



DMJ70H601SV3

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	Ι _D T _C = +25°C
700V	0.6Ω @ V _{GS} = 10V	8A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

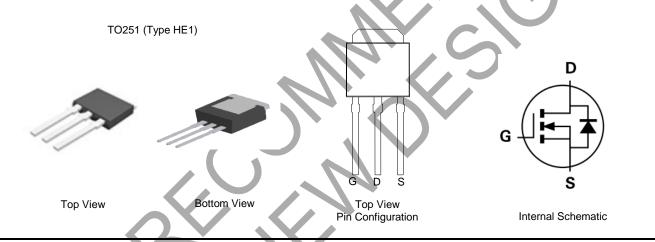
- Adaptor
- LCD & PDP TV
- Lighting

Features and Benefits Low On-Resistance

- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: TO251 (Type HE1)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.33 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMJ70H601SV3	TO251 (Type HE1)	75 Pieces / Tube

EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	700	V	
Gate-Source Voltage		V _{GSS}	±30	V
Continuous Drain Current (Note 5) $V_{GS} = 10V$	T _C = +25°C T _C = +100°C	ID	8 6.4	А
Maximum Body Diode Forward Current (Note 6)		Is	4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		IDM	15	А
Avalanche Current (Note 7)	L = 60mH	I _{AS}	1.7	А
Avalanche Energy (Note 7)	L = 60mH	E _{AS}	86	mJ
Peak Diode Recovery dv/dt (Note 7)		dv/dt	7	V/ns

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Cumhal	Malus	11
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	- Po	125 50	W
Thermal Resistance, Junction to Ambient (Note 6)		R _{0JA}	72	°C/W
Thermal Resistance, Junction to Case (Note 5)		Rejc	1.0	C/W
Operating and Storage Temperature Range		TJ, T _{STG}	-55 to +150	°C
		\mathcal{D}		

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

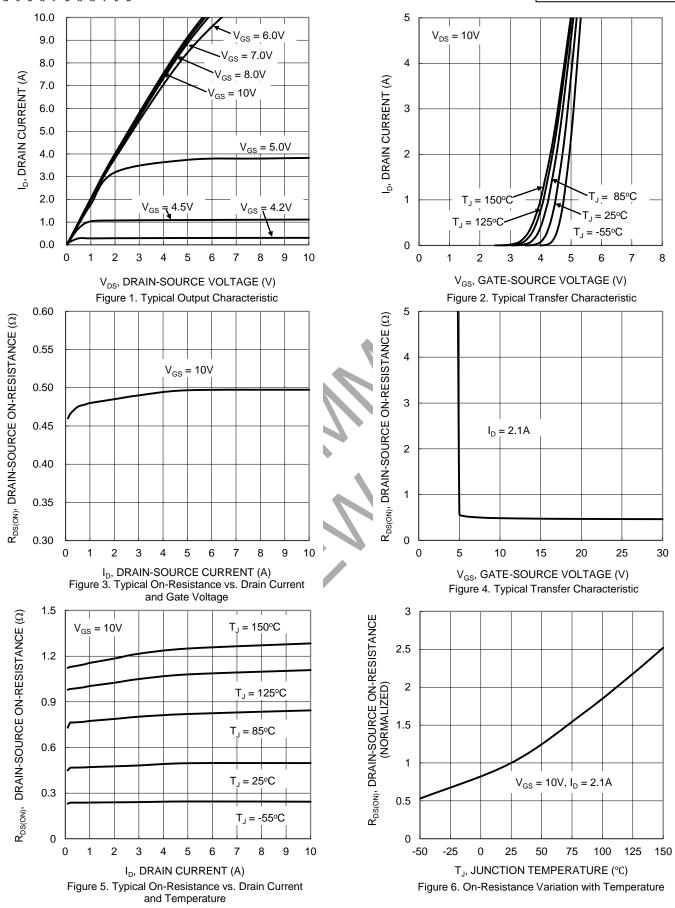
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	700	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	. —		1	μA	V _{DS} = 700V, V _{GS} = 0V	
Gate-Source Leakage	Igss			100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	2	3.4	4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	RDS(ON)		0.5	0.6	Ω	$V_{GS} = 10V, I_D = 2.1A$	
Diode Forward Voltage	V _{SD}		0.85	1.3	V	$V_{GS} = 0V, I_{S} = 2.1A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		686	_		V _{DS} = 50V, f = 1MHz,	
Output Capacitance	Coss	—	267	_	pF	$V_{\rm DS} = 50V$, $T = TWHZ$, $V_{\rm GS} = 0V$	
Reverse Transfer Capacitance	Crss	—	8	—		VGS = 0V	
Gate Resistance	R _G	—	2.6	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg	—	20.9	—			
Gate-Source Charge	Qgs	_	3.0	_	nC	$V_{DD} = 560V, I_D = 8A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q _{gd}	_	9.4	_			
Turn-On Delay Time	t _{D(ON)}		10	_			
Turn-On Rise Time	t _R		23	_	ns	V _{DD} = 350V, V _{GS} = 10V,	
Turn-Off Delay Time	t _{D(OFF)}		32	_	115	R _G = 4.7Ω, I _D =8A	
Turn-Off Fall Time	t _F	—	17	_			
Body Diode Reverse Recovery Time	t _{RR}		261	_	ns		
Body Diode Reverse Recovery Time $(T_J = +150^{\circ}C)$	t _{RR}		337	_	ns		
Body Diode Reverse Recovery Charge	Q _{RR}		3.0	_	μC	$I_{\rm S} = 8$ A, dl/dt = 100A/µs	
Body Diode Reverse Recovery Charge $(T_J = +150^{\circ}C)$	Q _{RR}	—	4.0	—	μC	7	

Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
 Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
 Guaranteed by design. Not subject to production testing.
 Short duration pulse test used to minimize self-heating effect.

Notes:

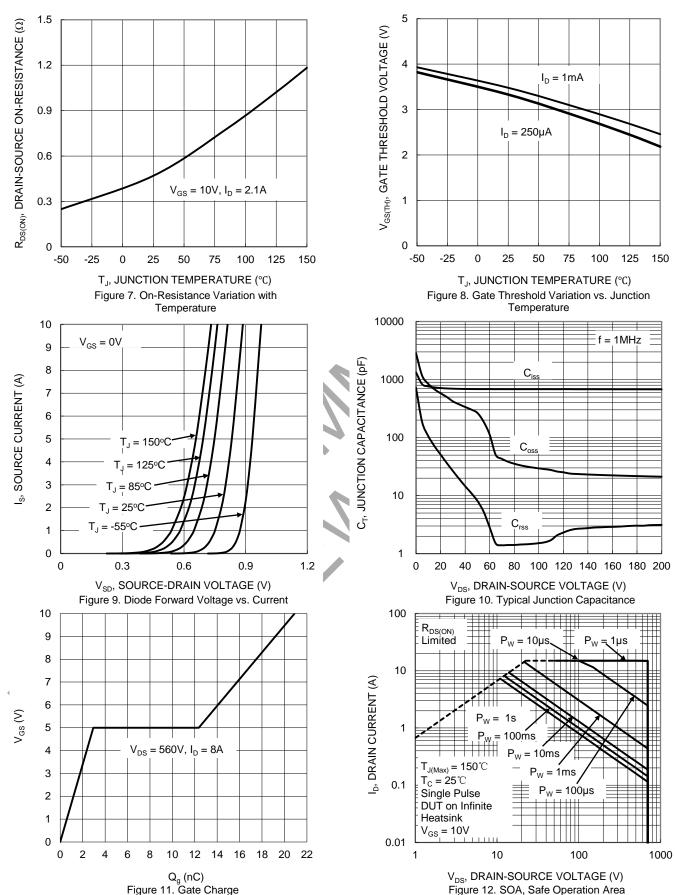


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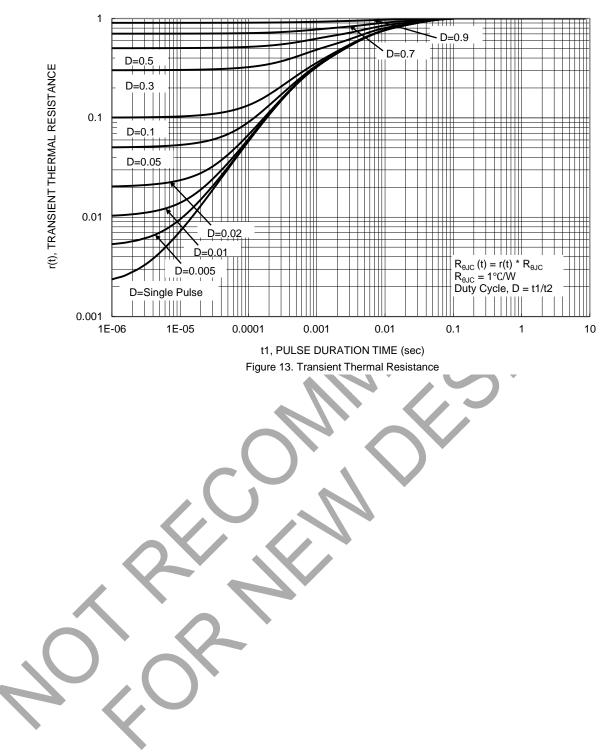


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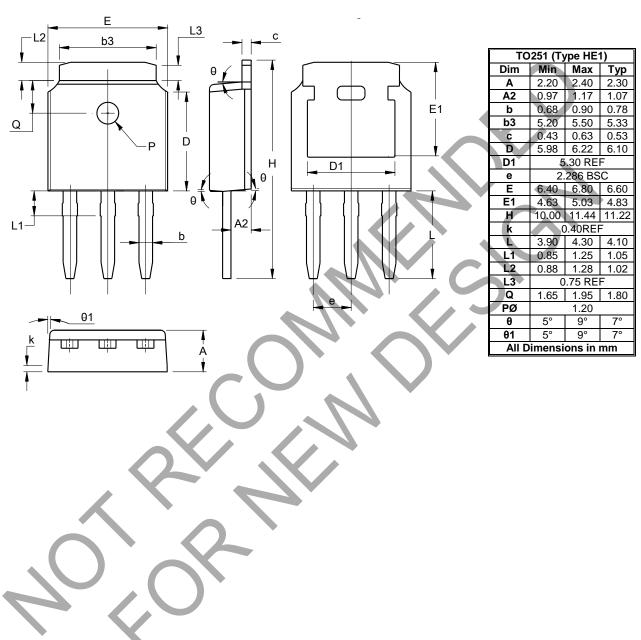






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





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