

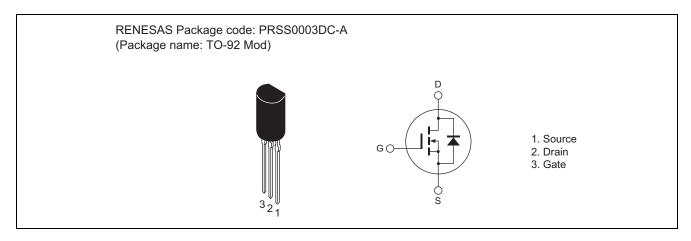
RJK4002DJE

400V - 3A - MOS FET High Speed Power Switching R07DS0842EJ0200 Rev.2.00 Aug 03, 2012

Features

- Low on-state resistance $R_{DS(on)}=2.4~\Omega~typ.~(at~I_D=1.5~A,~V_{GS}=10~V,~Ta=25^{\circ}C)$
- High speed switching

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Drain to source voltage	V_{DSS}	400	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I _D Note1	3	Α
Drain peak current	I _{D(pulse)} Note4	6	Α
Body-drain diode reverse drain current	I _{DR} Note1	3	Α
Body-drain diode reverse drain peak current	I _{DR(pulse)} Note4	6	Α
Avalanche current	I _{AP} Note3	2.5	Α
Avalanche energy	E _{AR} Note3	0.357	mJ
Channel dissipation	Pch Note 2	2.54	W
Channel to ambient thermal Impedance	θch-a	49.2	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Limited by Tch max.

- 2. Value at Tc = 25°C
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C
- 4. Pulse width limited by safe operating area.

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

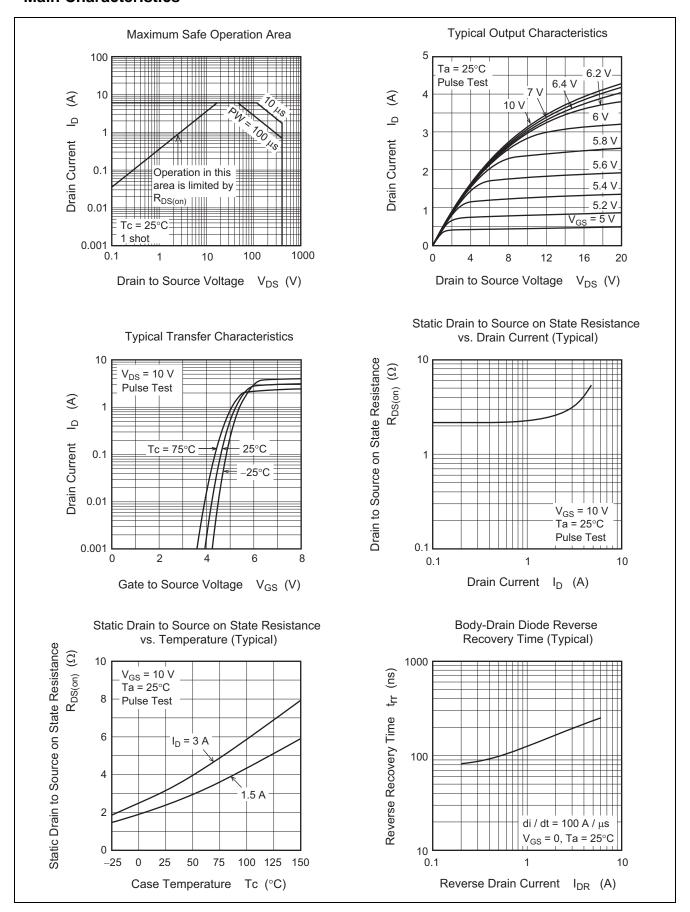
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	400	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 400 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	3.5	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}	_	2.4	2.9	Ω	$I_D = 1.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 5}}$
Input capacitance	Ciss	_	165	_	рF	V _{DS} = 25 V
Output capacitance	Coss	_	25	_	рF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	2.6	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	11	_	ns	I _D = 1.5 A
Rise time	t _r	_	12	_	ns	$V_{GS} = 10 \text{ V}$ $R_L = 133 \Omega$ $Rg = 10 \Omega$
Turn-off delay time	t _{d(off)}	_	23	_	ns	
Fall time	t _f	_	20	_	ns	
Total gate charge	Qg	_	6.0	_	nC	V _{DD} = 320 V
Gate to source charge	Qgs	_	1.2	_	nC	$V_{DS} = 100 \text{ V}$ $I_{D} = 3 \text{ A}$
Gate to drain charge	Qgd	_	3.4	_	nC	
Body-drain diode forward voltage	V_{DF}	_	0.9	1.5	V	$I_F = 3 \text{ A}, V_{GS} = 0^{\text{Note 5}}$
Body-drain diode reverse recovery time	t _{rr}	_	200	_	ns	$I_F = 3 A, V_{GS} = 0$
						di _F /dt = 100 A/μs

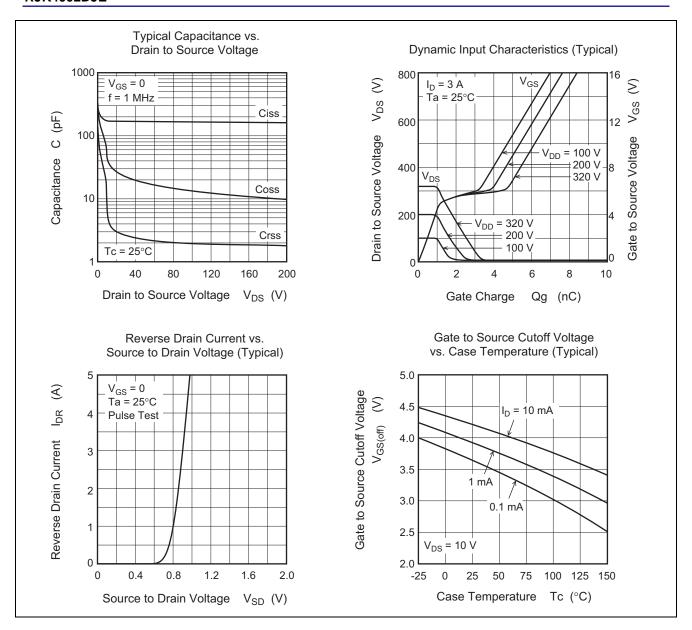
Note: 5. Pulse test

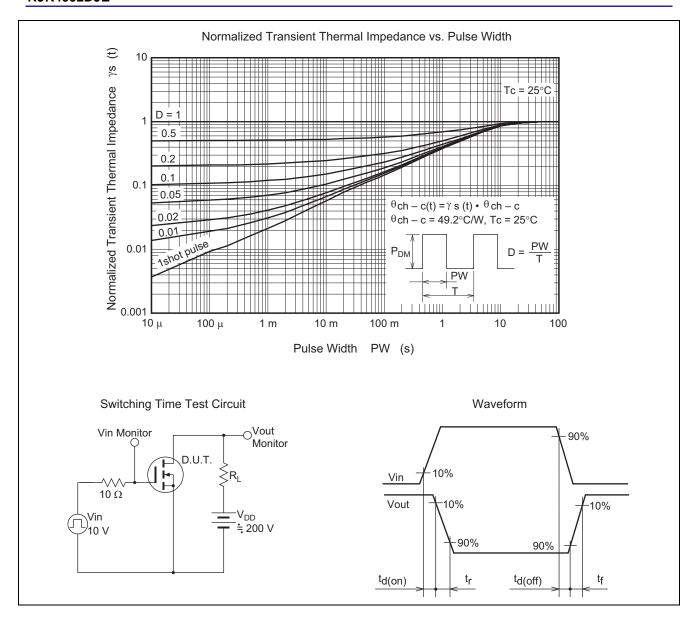
- 6. Since this device is equipped with high voltage FET chip (V_{DSS} ≥ 400 V), high voltage may be supplied. Therefore, please be sure to confirm about electric discharge between drain terminal and other terminal.
- 7. This device is sensitive to electrostatic discharge.

 It is recommended to adopt appropriate cautions when handling this product.

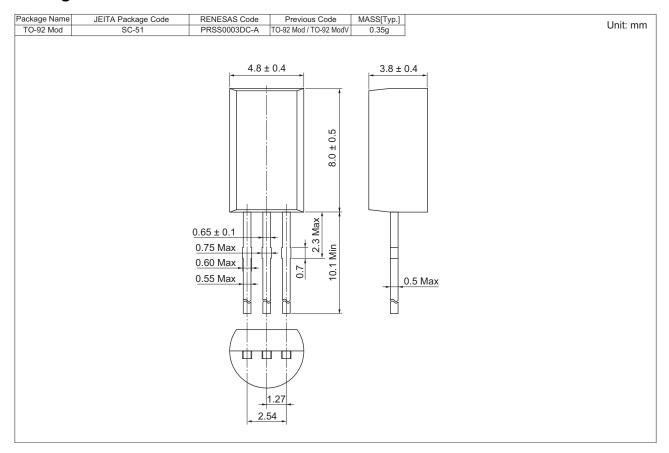
Main Characteristics







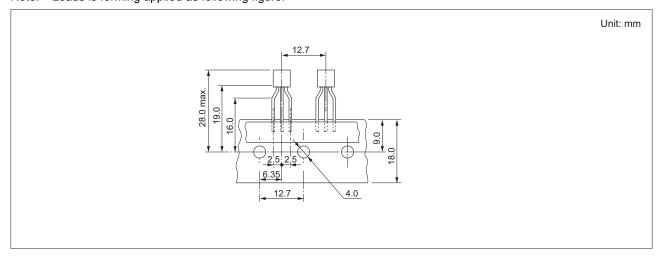
Package Dimensions



Ordering Information

Orderable Part No.	Quantity	Shipping Container
RJK4002DJE-00#Z0	2500 pcs	Hold Box, Radial Taping

Note: Leads is forming applied as following figure.



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